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## APPLICANTS

Stefan Scholl, Munich, GERMANY;

\*\* CONTINUING DATA *None NL*\*\* FOREIGN APPLICATIONS *Yes NL*

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## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	GERMANY	DRAWING 4	CLAIMS 17	CLAIMS 2
Verified and Acknowledged	<i>Norin</i> <i>NL</i> Examiner's Signature Initials				

## ADDRESS

04743  
 MARSHALL, GERSTEIN & BORUN LLP  
 233 S. WACKER DRIVE, SUITE 6300  
 SEARS TOWER  
 CHICAGO , IL  
 60606

## TITLE

Process for designing and manufacturing semi-conductor memory components, in particular dram components

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